



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/774,362

Filing Date: February 6, 2004

Applicants: Yoshihide Matsuo

Group Art Unit: 2815

Examiner: WARREN, Matthew E

Title: MANUFACTURING METHOD FOR SEMICONDUCTOR
DEVICE, SEMICONDUCTOR DEVICE, AND ELECTRONIC
APPARATUS

Attorney Docket: 9319G-000679

Director of the U.S. Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

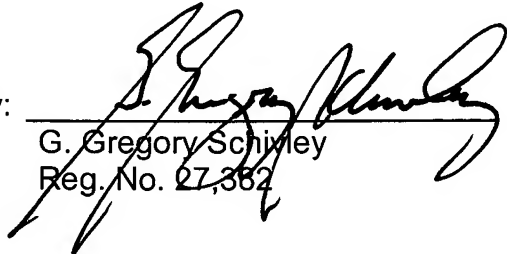
In response to the restriction requirement mailed December 16, 2004, Applicants provisionally elect the claims of Group II (claims 1-19), with traverse.

The Examiner is requested to reconsider the restriction requirement since all groups of claims are sufficiently related to each other that an undue burden would not be placed upon the Examiner by maintaining all groups in a single application. MPEP § 803

Favorable consideration of this application is respectfully requested.

Respectfully submitted,

Dated: Jan 12, 2005

By: 
G. Gregory Schirley
Reg. No. 27,382

HARNESS, DICKEY & PIERCE, P.L.C.
P.O. Box 828
Bloomfield Hills, Michigan 48303
(248) 641-1600